

 File View Edit Tools Window Help




**RAFT**      **BRS:**

**BRS:**

**BRS:**

Left BRS:

Left BRS:

 Pending

☒ Active

L1: (1) ("6466898").PN.

L2: (4) ("6466898") or ("6425762") or ("6339836") or ("6108494")

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L3: (0) 2 and (processor and (chip adj set))

```

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L4: (0) 2 and (processor and (chip adj set))

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```

L5: (586) processor and (chip adj set) and simulat$4

```

✶ L6: (38) 5 and backplane

 L7: (12) 6 and (data near5 format)

```
L8: (10) motherboard adj simulat$4
```

 Failed

  Saved

 Favorites

 Tagged (1)

UDC

**Queue**

 Trash

	U	1	Document ID	Issue Date	Pages	Title
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6484281 B1	20021119	9	Software-based simulation simulating the combined fu
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6421251 B1	20020716	133	Array board interconnect s
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6389379 B1	20020514	157	Converification system and
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6321366 B1	20011120	165	Timing-insensitive glitch- method

 **Details**

 Image

 Text

 HTML

Ready

 Inbox - Microsoft...

**A EAST - [Dwi...**

## EAST Browser

